Docket No.: 43876-153 **PATENT**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of : Customer Number: 20277

Craig HANSEN, et al. : Confirmation Number: To be assigned

Serial No.: Unknown : Group Art Unit: To be assigned

Continuation of

Application No. 10/646,787

Filed August 25, 2003

Filed: January 16, 2004 : Examiner: To be assigned

For: MULTITHREADED PROGRAMMABLE PROCESSOR AND SYSTEM WITH PARTITIONED OPERATIONS

INFORMATION DISCLOSURE STATEMENT

Mail Stop NEW APPLICATIONS Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached form PTO-1449. It is respectfully requested that the references be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

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earlier filing date under 35 USC 120. Thus, copies of these references are not attached. 37 CFR

1.98(d).

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Respectfully submitted,

MCDERMOTT, WILL & EMERY

Lawrence T. Cullen

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Date: January 16, 2004

WDC99 845008-1.043876.0153

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INFORMATION DISCLOSURE CITATION IN AN APPLICATION				ATTY. DOCKET NO. 43876-153 SERIAL NO. Continuation of S rial No. 10/646,787					
			APPLICANT HANSEN, et al.						
	(PTO-	1449)	FILING DATE January 16, 2004		GROUP To be assigned				
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EXAMINER'S INITIALS	PATENT NO.	DATE	T	NAME	CLASS	SUBCLASS	FIL	ING DATE	
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	4,489393	12/18/84	Kawahara, et	al.					
	4,701,875	10/20/87	Konishi, et al	l.	1	1			
	4,727,505	02/23/88	Konishi, et al	Ī.		 			
	4,876,660	10/24/89	Owens, et al.			<u> </u>		 	
	4,893,267	01/09/90	Alsup, et al.		1	1			
	4,956,801	09/11/90	Priem et al.		1				
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		of the Motore		erscalar RISC M	licroproce	essor, Keith	Diefend	lorff and	
	Lead with Pov	wer2, Six Wa	ay Superscalar	per 13, October 4 CPU in MCM A	Achieves 1	126 SPECint	t92.		
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	(PTO-1	1449)		FILING DATE January 16, 2004		GROUP To be assigned		
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WDC99 864084-1.043876.0153

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

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SHEET 1 OF 1 INFORMATION DISCLOSURE ATTY. DOCKET NO. SERIAL NO. 43876-153 Continuation of Serial No. CITATION IN AN 10/646,787 APPLICATION **APPLICANT** Craig HANSEN, et al. FILING DATE **GROUP** (PTO-1449) January 16, 2004 To be assigned U.S. PATENT DOCUMENTS EXAMINER'S Document Number Publication Date Name of Patentee or Applicant of Cited Pages, Columns, Lines, Where Relevant INITIALS CITE MM-DD-YYY Document Passages or Relevant Figures Appear Number-Kind Code2 (# known) US 4,785,393 11/15/1988 Chu et al us 4,814,976 03/21/1989 Craig C. Hansen, et al. US 5,031,135 07/09/1991 Patel US 5,280,598 01/1994 Osaki et al. US 5,481,686 01/02/1996 Dockser US 5,487,024 01/1996 Girardeau Jr. US 5,600,814 02/1997 Gahan et al. US 5,740,093 04/14/1998 Sharangpani US 5,742,840 04/21/1998 Hansen et al. US 5,768,546 06/1998 Kwon US 5,898,849 04/27/1999 Tran US 5,996,057 11/30/1999 Hunter L. Scales, III, et al. US 6,041,404 03/21/2000 Patrice Roussel, etla. US 6,052,769 04/18/2000 Thomas R. Huff, et al US 6,173,393 B1 01/09/2001 Salvador Palanca, et al. US 6,275,834 B1 08/14/2001 Derrick Chu Lin, et al US 6,295,599 09/2001 Hansen et al. FOREIGN PATENT DOCUMENTS **EXAMINER'S** Foreign Patent Document **Publication Date** Name of Patentee or Applicant Pages, Columns, Lines Translation INITIALS CITE Country Codes - Number 4 - Kind of Cited Document Where Relevant Figures MM-DD-YYYY Codes (if known) Appear Yes Νo OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.) Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, EXAMINER'S INITIALS serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. CITE IEEE Draft Standard for "Scalable Coherent Interface-Low-Voltage Differential Signal Specifications And Packet Encoding", IEEE Standards Department, P1596.3/D0.15 (March 1992) IEEE Draft Standard for "High-Bandwidth Memory Interface Based on SCI Signaling Technology (RamLink)", IEEE Standards Department, Draft 1.25 IEEE P1596.4-199X (May 1995) IBM, "The PowerPC Architecture: A Specification For A New Family of Risc Processors", 2nd Ed., Morgan Kaufmann Publishers, Inc., (1994)Hewlett-Packard Co., "PA-RISC 1.1 Architecture and Instruction Set", Manual Part No. 09740-90039, (1990). MIPS Computer Systems, Inc., "MIPS R4000 User's Manual", Mfg. Part No. M8-00040, (1990) **EXAMINER** DATE CONSIDERED

^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached. WDC99 864088-1.043876.0153

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Lawrence T. Cullen Registration No. 44,489

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					APPLICANT Craig HANSEN et al					
		(PT	O-1449)		FILING DATE GROUP Jan. 16, 2004 To be assigned			igned		
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